Switch Cap Divided to Analog Converter

Hardware registers missing, use of older bus

x = ADC value;

for (i = 0; i < 10; i++) {
    delay 10 clocks
    ADC - sample();
    infinite sampling
    ADC - in - select = BAT;
    // or TEMP or INV
    PDA - gain = 4;
    if (i < 8) Write angles input
}

Week 15 (Read) 1/C class

Mixed Results for feedback on style
Week 14

Series R - Equations
Log 3G results
High level block diagram

Week 13 - From introductory, something intermediate

Refined Presentations