Problem: Charge Injection as Switch Not NOK or MOS

Minimum size Pmos 100\(\mu\)m

Reset OK: Big Current 2.5 FmF

\(V_e\) = 5V

Check Inversion

- Discharge timing
- Acceptance tests, timing, BIST
- Inter sort - die level testing of chips

If I designed, I calculated it measured
Preconditions on some: Good, me, no mkdir

Finish design due 2 weeks (9 AM Monday)